

FORM PTO-1449 (Rev. 2-32)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No.	Serial No.
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)		01-181-A	10/629,167
		Applicant: Huy Nguyen, et al.	
		Filing Date: 07/28/03	Group: 2819



## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
DC	5,534,795	07/09/96	Wert et al.	326	81	12/09/94
DC	5,534,798	07/09/96	Phillips et al.	326	108	06/06/95
DC	5,663,663	09/02/97	Cao et al.	326	81	04/26/96
DC	5,751,168	05/12/98	Speed, III et al.	326	83	04/28/95
DC	5,757,712	05/26/98	Nagel et al.	365	226	07/12/96
DC	5,867,010	02/02/99	Hinedi et al.	323	282	06/06/97
DC	5,973,508	10/26/99	Nowak et al.	326	81	05/21/97
DC	5,986,472	11/16/99	Hinedi et al.	326	68	06/06/97
DC	6,097,215	08/01/00	Bialas, Jr. et al.	326	68	05/22/98
DC	6,160,421	12/12/00	Barna	326	63	10/22/98
EXAMINER	<i>Paul D. Chay</i>		DATE CONSIDERED	<i>5/26/04</i>		

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

FORM PTO-1449 (Rev. 2-32)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket N .	Serial N .
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		01-181-A	10/629,167
Applicant: Huy Nguyen, et al.			
Filing Date: 07/28/03		Group: 2819	

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translati n	
							Yes	N

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

DC	"Direct Rambus Clock Generator", Rambus Inc., Document DL-0056, Version 1.2, November 2000, pages 1-22.
DC	"Application Brief #1", IDT Logic Applications, Integrated Device Technology, Inc., APP-BRF1-00050, 2000, pages 1-4.
DC	Jim Hagerman, "Two Transistors Form Bidirectional Level Translator", <a href="http://www.ednmag.com/reg/1996/110796/23DI_02.htm">www.ednmag.com/reg/1996/110796/23DI_02.htm</a> , 1996, pages 1-3.
DC	"The PC Technology Guide", <a href="http://www.pctechguide.com/03memory.htm">www.pctechguide.com/03memory.htm</a> , February 18, 2001, pages 1-9.
DC	"System Management Bus (SMBus) Specification Version 2.0", Section 2 General Characteristics, SBS Implementers Forum, August 3, 2000, pages 9-10.
DC	Kenneth M. Cuy, "Design Considerations Bring Unity To A Mixed-Voltage World", <a href="http://www.ednmag.com/ednmag/reg/1995/020295/03df3.htm">www.ednmag.com/ednmag/reg/1995/020295/03df3.htm</a> , February 2, 1995, pages 1-8.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.